

Lecturer Muhammad Haris Anis

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Experience

1. More than Eight years experience in the Design and Development of Embedded Systems
2. More than Eight years in the Design, Development, Verification and Documentation of hardware for ASICs/FPGAs in Verilog/System Verilog
3. One year experience in Teaching of Electrical Engineering courses at undergraduate level.

Education

1. MS Computer Engineering, UET Taxila
2. BE Computer System Engineering, College of E&ME, NUST