Muhammad Shuaib

Education:

MSc Electrical Engineering, System on Chip (Integrated Circuits), **2 Years**
Linköping University Sweden, (2009)
BSc Electrical Engineering (Major in Electronics and Communication); 4 Years
University of Engineering and Technology Taxila.
BSc (Maths A&B, Physics); Two Years, University of the Punjab Lahore

Experience:

1: Teaching Experience (01-01-2010 to date):
I am Assistant Professor at International Islamic University Islamabad, in Electrical engineering department; with responsibilities research and teaching.

2003- To- 2006 Assistant Manager (Electrical Engineer), GEPCO
I have been working as Assistant Manager in (Gujranwala Electric Power Company), which is one of the companies, which provides and distributes electricity to the consumers. I was member of the team whose responsibilities were to carry out testing and troubleshooting, especially the protection and instrumentation wing of the system.

2002- To -2003 Assistant Director, Punjab Industries (Electrical Engineer), I have been working as Assistant Director in Punjab Industries, Mines and Mineral Department.
2: I have also experience of designing integrated circuits (Full Custom design) from system level to tape out of an IC and also testing of post fabrication of chip**.**

Publications:

1:Two Tone PLL For On- Chip RF Test
https://www.google.com.pk/?gws_rd=ssl#q=http:%2F%2Fwww.essays.se%2Fessay%2F12ba6b571a%2F
http://www.amazon.com/Two-Tone-PLL-On-Chip-RF-Test/dp/3639180895

2: "92dB DC-Gain Two-Stage Class AB Fully- Differential Op-amp"
2020 IEEE The 5th International Conference on Integrated Circuits and Microsystems.

Interest Area:

Analog/Mixed-Signal IC design, RF/Analog circuits for wireless communications. Low-Power analog circuit design. Delta-Sigma Converters, Nyquist rate data converters, Phase locked loops and CMOS Image Sensors

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EDA Tools Experience:

Cadence (HDL, Schematic, Virtuoso, LVS, Assura, Calibre, Spectre /Spectre RF), Silvaco T Cad, Mentor graphic IC Design. ADS.

Other Relevant Skills:

Knowledge in high-level circuit modeling languages such as VHDL, Verilog, and Verilog-A. Knowledge in Linux system, System C. Mat Lab