


# KHIZER MEHMOOD

	<p> <b>Date of Birth:</b> 24-02-1988  <b>Nationality:</b> Pakistani  <b>Marital Status:</b> Married  <b>PEC No:</b> ELECTRO/12957  <b>CNIC:</b> 37406-9207054-9  <b>Email:</b> khizer.mehmood@iiu.edu.pk  <b>Cell:</b> 0322-5293101  <b>Address:</b> House#54,Street#2,Block F, Newcity Phase-II, Wah Cantt         </p>
<p style="text-align: center;"><b>Personal Information</b></p>	
<p style="text-align: center;"><b>Qualifications</b></p>	<ul style="list-style-type: none"> <li>❖ PhD in Electronic Engineering (<b>2014 - In Progress</b>)             <ul style="list-style-type: none"> <li>➤ International Islamic University Islamabad                 <ul style="list-style-type: none"> <li>• CGPA: <b>3.50</b>/4.00</li> </ul> </li> </ul> </li> <li>❖ Master of Science in Electrical Engineering (<b>2013</b>)             <ul style="list-style-type: none"> <li>➤ University of Engineering &amp; Technology, Taxila                 <ul style="list-style-type: none"> <li>• CGPA:<b>3.00</b>/4.00</li> </ul> </li> </ul> </li> <li>❖ Bachelor of Science in Electronics Engineering (<b>2010</b>)             <ul style="list-style-type: none"> <li>➤ International Islamic University Islamabad                 <ul style="list-style-type: none"> <li>• CGPA: <b>3.25</b>/4.00 Percentage:<b>75.10%</b>(Grade:<b>B+</b>)</li> </ul> </li> </ul> </li> <li>❖ HSSC Pre-Engineering (<b>2006</b>)             <ul style="list-style-type: none"> <li>➤ FBISE Islamabad                 <ul style="list-style-type: none"> <li>• Marks:<b>741</b>/1100 Percentage: <b>67.3 %</b> (Grade: <b>B</b>)</li> </ul> </li> </ul> </li> <li>❖ SSC Science (<b>2004</b>)             <ul style="list-style-type: none"> <li>➤ BISE Rawalpindi                 <ul style="list-style-type: none"> <li>• Marks:<b>510</b>/850 Percentage: <b>60%</b> (Grade: <b>B</b>)</li> </ul> </li> </ul> </li> </ul>
<p style="text-align: center;"><b>Experience</b></p>	<ul style="list-style-type: none"> <li>❖ Working as <b>Lecturer</b> from <b>17-3-2014</b> to <b>present</b> in Department of Electrical Engineering, Faculty of Engineering &amp; Technology, IIU Islamabad</li> <li>❖ Worked as a <b>M.Sc. Full Time Scholar</b> from <b>15-08-2011</b> to <b>15-02-2013</b> in Electrical &amp; Electronics Engineering Department, UET TAXILA.</li> </ul>
<p style="text-align: center;"><b>Awards and Scholarships</b></p>	<p>Awarded with M.Sc. Electrical Engineering Full Time Scholarship from UET Taxila.</p>
	<ul style="list-style-type: none"> <li>❖ Visual Object Tracking using Spatio Temporal Information(PhD).</li> </ul>

<b>Projects</b>	<p>Thesis)</p> <ul style="list-style-type: none"> <li>❖ Design and Implementation of Face Detection System(M.Sc. Thesis)</li> <li>❖ Smart Home Automation and Security System(B.Sc. Final Year Project)</li> </ul>
<b>Courses Taught</b>	<ul style="list-style-type: none"> <li>❖ Power Electronics</li> <li>❖ Probability and Random Variables</li> <li>❖ Circuit Analysis-I</li> <li>❖ Electronic Circuit Design-I</li> </ul>
<b>Research Publication</b>	<p>Jadoon, Baber &amp; Ali, Ahmad &amp; Jalil, Abdul &amp; Mehmood, Khizer &amp; Murad, Maria &amp; Awan, Hamdan. (2020). AFAM-PEC: Adaptive Failure Avoidance Tracking Mechanism using Prediction-Estimation Collaboration. IEEE Access. PP. 1-1. 10.1109/ACCESS.2020.3015580.</p> <p>Murad, Maria &amp; Bilal, Muhammad &amp; Jalil, Abdul &amp; Ali, Ahmad &amp; Mehmood, Khizer &amp; Jadoon, Baber. (2020). Efficient Reconstruction Technique for Multi-Slice CS-MRI Using Novel Interpolation and 2D Sampling Scheme. IEEE Access. PP. 1-1. 10.1109/ACCESS.2020.3004731.</p> <p>Adil Farooq, Umair Ali, Awais Khan Shinwari, Ashfaq ur Rehman, Suhail Ahmed, <b>Khizer Mehmood</b> and Waseem Iqbal.” SCADA Based Power Management and Distribution System Prototype for Pakistan Scenario,” in <i>Proceedings of the IEEE Conference on PGSRET</i>, 2018, (Accepted).</p>
<b>Administrative Tasks</b>	<ul style="list-style-type: none"> <li>❖ Curriculum Revision Committee (Secretary)</li> <li>❖ Undergraduate Committee (Member)</li> <li>❖ DEE-PEC Committee (Member)</li> <li>❖ OBE Software Committee (Member)</li> <li>❖ DQAC Committee (Member)</li> <li>❖ Compiled OBE based Curriculum of BS Electrical Engineering.</li> <li>❖ Involved in OBE based evaluation of BS Electrical Engineering.</li> <li>❖ Compiled Courses Allocation and Scheme of Studies</li> <li>❖ Worked in various administrative committees in IIU Open House Exhibition.</li> <li>❖ Worked in various administrative committees in IIU FETEX Exhibition.</li> <li>❖ Worked in various administrative committees in IIU Spectra Exhibition.</li> <li>❖ Worked in various administrative committees in IIU ICISE Conference.</li> </ul>
	<ul style="list-style-type: none"> <li>❖ Creative, Hardworking, Sincere, Self-starter and Dedicated</li> </ul>

<b>Technical and Professional Skills</b>	<ul style="list-style-type: none"> <li>❖ Communication in both English &amp; Urdu</li> <li>❖ Object Oriented Programming in C &amp; C++</li> <li>❖ Schematic Circuit Simulation using Proteus.</li> <li>❖ Embedded System Programming in C &amp; Assembly Language.</li> <li>❖ Digital Design Simulation &amp; Synthesis in Verilog HDL (Quartus II, Xilinx ISE, Modelsim)</li> <li>❖ Mathematical modeling in MATLAB and Simulink</li> <li>❖ Document Editing in MS Office (Word, PowerPoint, Excel, Publisher)</li> </ul>
<b>References</b>	<ul style="list-style-type: none"> <li>❖ Available on Demand</li> </ul>